

APPL NUM 10002100	FILING DATE 11/14/2001	CLASS 438	SUBCLASS 692	GAU 1765	EXAMINER <i>Utech</i>
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ga

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A DIV OF 09/447,309 11/22/1999 *pat. # 6352927*
ga

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 10-335642 11/26/1998 *ga*
2002/0037650

<input checked="" type="checkbox"/> PG-PUB	<input type="checkbox"/> DO NOT PUBLISH	<input type="checkbox"/> RESCIND
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>ga</i>		ATTORNEY DOCKET NO 81839.0106

TITLE : Semiconductor wafer and method for fabrication thereof

U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
<input type="checkbox"/> TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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